

PH. recall def $\Sigma^i P$ $L \in \Sigma^i P$ if $\exists TM M$ runs in poly time st

$$L = \{x : \exists y_1, y_2 \dots Q^i y_i M(x, y_1 \dots y_i) = 1\}$$

prop. if $\Sigma^i P = \Pi^i P$ for some i then $PH = \Sigma^i P$
if $P = NP$ then $PH = P$.

sketch: $\Sigma^{i+1} P = \Sigma^i P$

$$x : \exists y_1, y_2 \dots Q^i y_i Q^{i+1} y_{i+1} M(\dots) = 1$$

i quant. start w/ \exists . $\rightarrow (x, y_1)$ is a by. in $\Pi^i P$.
" $\Sigma^i P$.

$$x : \exists y_1, y_2 \dots Q^i y_i Q^{i+1} y_{i+1} \text{ so } L \in \Sigma^i P. \quad \#$$

$\Sigma_1 = NP; \Sigma_2 = NP^{NP} = NP^{\Sigma_1} \quad \Sigma_3 = NP^{\Sigma_2} \quad \dots \quad \Sigma_{i+1} = NP^{\Sigma_i}$

proof. $i=1$ by def.
($i=2$).

\subseteq pick $L \in \Sigma_2 P$ by def $\exists M L = \{x : \exists y \forall z M(x, y, z) = 1\}$.

$$L' = \{(x, y) \forall z M(x, y, z) = 1\}. \quad L' \in \Pi_1 P = coNP.$$

build N nondet. poly time, oracle L' : input x : guess y .
 N decide $L \Rightarrow L \in NP^{NP}$.
query if $(x, y) \in L'$

\subseteq pick $L \in NP^{NP}$ by def $\exists M$ guesses γ and makes queries $g_1 \dots g_k$.
oracle N receives answers $a_1 \dots a_k$.

if g_i, a_i $\begin{cases} 1 \exists u_i \text{ st } N(g_i, u_i) = 1 \\ 0 \forall u_i \quad N(g_i, u_i) = 0. \end{cases}$

write in $\Sigma^2 P$ form, ie: $\exists \dots \forall \dots M'(\dots)$

$$\exists y, g_1, \dots, g_k, a_1, \dots, a_k, u_1, \dots, u_k, v_1, \dots, v_k:$$

$\rightarrow M$ accept (with $y, g_1 \dots g_k, a_1 \dots a_k$) (simulate M) \checkmark .

$\rightarrow a_i = 1 \Rightarrow N(g_i, u_i) = 1 \quad \checkmark$.

$\rightarrow a_i = 0 \Rightarrow N(g_i, v_i) = 0 \quad \checkmark$.

$L \in \Sigma^2 P \quad \#$

Circuits

why? black-box TM \Rightarrow cannot distinguish $P=NP$ from $P \neq NP$.

\exists oracles A, B st $P^A = NP^A, P^B \neq NP^B$.

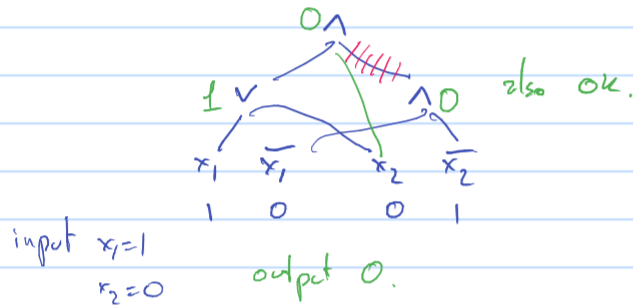
circuits can analyze structure: hope?

def circuit: (for input of size n): DAG (directed acyclic graph) vertices have labels
 \wedge AND \vee OR.

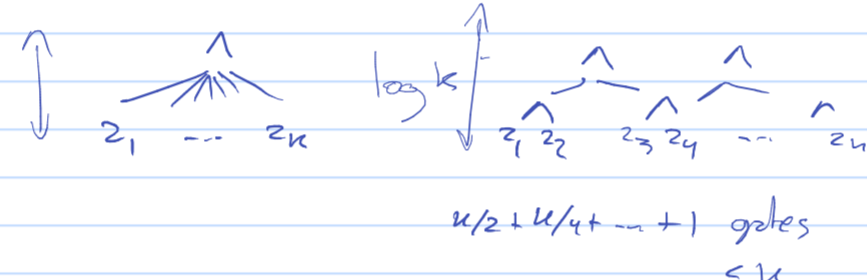
$2 \cdot n$ source vertices (without incoming edges)

1 sink (— outgoing edges).

internal vertices have exactly 2 incoming edges (indegree 2).

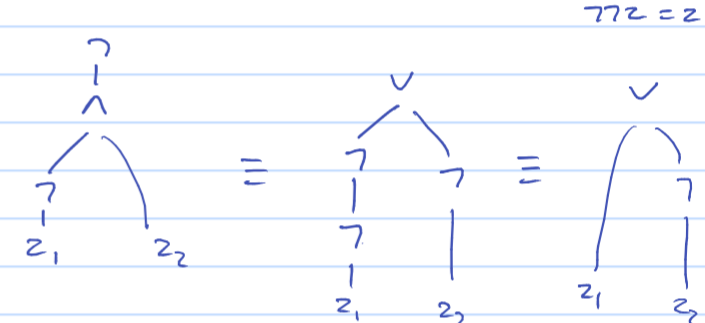
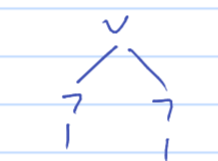
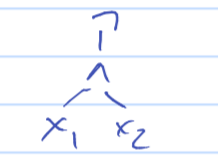


\rightarrow obs: indegree 2 does not matter



(unless limited circuits).

\rightarrow obs: negations at sources/inputs.



\rightarrow obs: for each n can have a different circuit. "non-uniformity".

prop: circuits can compute every unary language.

$$L = \{1^n \text{ for } n \in X\}. \quad \text{if } n \in X \text{ then } C_n = \bigwedge x_1 \dots x_n. \quad \text{if } n \notin X \text{ then } C_n = 0.$$

def $P/poly$: languages that can be decided by a family of circuits of polynomial size.

$P \neq P/poly$.

thm $P \subseteq P/poly$.

def family of circuits is uniform if $\exists TM M$ that on input 1^n outputs C_n

prop languages decidable by uniform circuits = P .

sketch: input x : use M to build $C_{|x|}$, then eval. $C_{|x|}(x)$.

are there TMs equivalent to circuits? yes! TMs with advice.

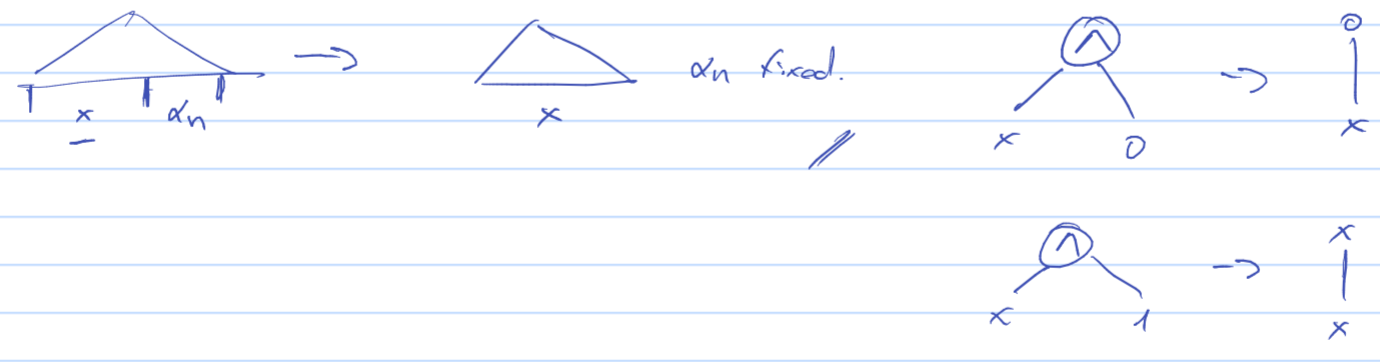
exists a DB of advice (sequence $\alpha_n, |\alpha_n| = poly(n)$) M runs on input $(x, \alpha_{|x|})$

$$C_{f(n)} = C \text{ with advice of size } \leq f(n) \quad P/poly$$

prop $P/poly$ is the same as languages decidable by circuits of poly size.

sketch: $L \in C_n$. advice $\alpha_n = C_n$. $M(x, \alpha_n)$ simulate C_n on x .

$L \in P/poly$. build circuit that simulates $M(x, \alpha_n)$.



$P \neq P/poly$. $NP?$

is $NP \subseteq P/poly$? unlikely!

thm (Karp-Lipton) If $NP \subseteq P/poly$ then $PH = \Sigma^2 P$.

proof: enough to show $\Sigma^2 P = \Pi^2 P$. Π^2 -SAT is $\Pi^2 P$ -complete.

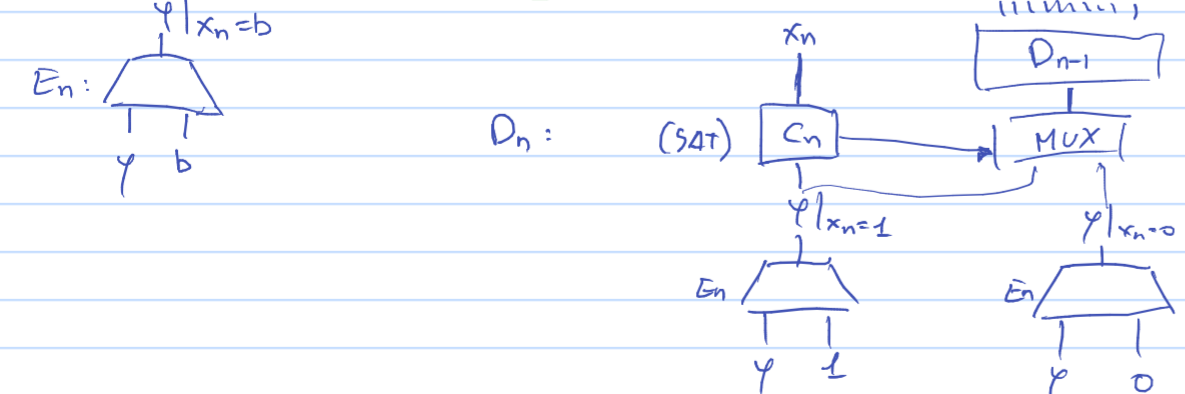
Π^2 -SAT is in $\Sigma^2 P$

$$\{ \exists z \forall y \exists \bar{y} \varphi(\bar{z}, \bar{y}) \}$$

assume $NP \subseteq P/poly$: $\exists \{C_n\}$ that decide SAT.

$$\forall \varphi, |\varphi|=n, C_n(\varphi) = 1 \text{ iff } \varphi \text{ is SAT.}$$

self-reducibility: $\exists \{D_n\}$ given φ output y st $\varphi(y) = 1$.



$\Sigma^2 P$ language: given $z = \langle x, y \rangle$.

$$\exists D_n \forall \varphi, x : \rightarrow \varphi(D_n(\varphi)) \wedge \rightarrow D_n(\varphi|x) \neq \emptyset.$$

Π^2 -SAT $\in \Sigma^2 P$.
hence $\Sigma^2 P = \Pi^2 P$
 $PH = \Sigma^2 P$ //

thm [Shannon] $\exists f: \{0,1\}^n \rightarrow \{0,1\}$ req. circuits of size $2^n/10n$.

proof: counting. #functions 2^{2^n} .

circuits on s gates? $s \cdot (2 \log s + 1) \leq 3s \log s$. $\log s$ $\log s$.
#circuits $\leq 2^{3s \log s}$.

$3s \log s \leq 2^n \rightarrow$ not enough circuits!

$$\downarrow$$

$$s > 2^n / 10n$$