

CDCL vs Resolution

Marc Vinyals

$$y \vee z \quad y \vee \bar{z} \quad x \vee \bar{y} \vee z \quad x \vee \bar{y} \vee \bar{z} \quad \bar{x} \vee \bar{y}$$

Algorithm 1: DPLL

while *not solved* **do**

if *conflict* **then** backtrack()

else if *unit* **then** propagate()

else branch()

State: partial assignment

DPLL

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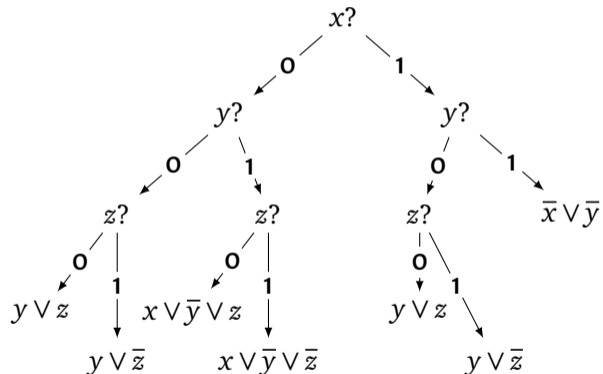
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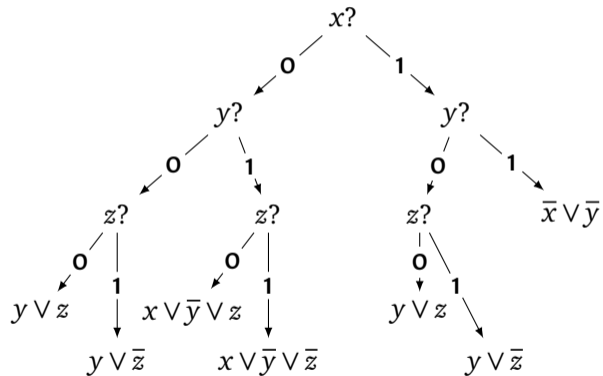
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Resolution

- Interpret DPLL run as resolution proof

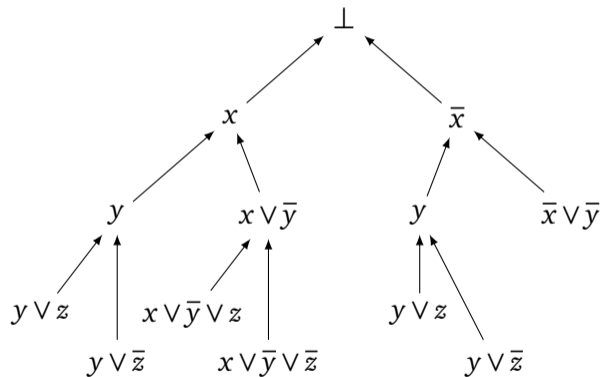
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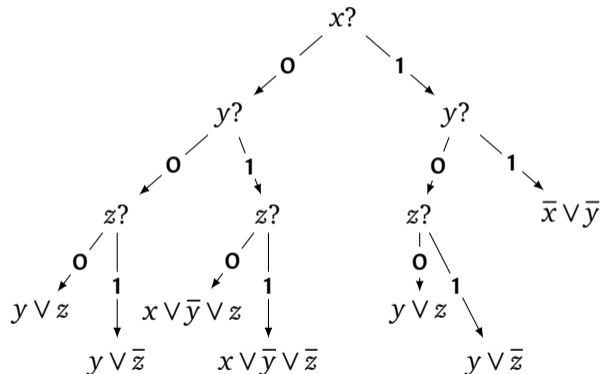
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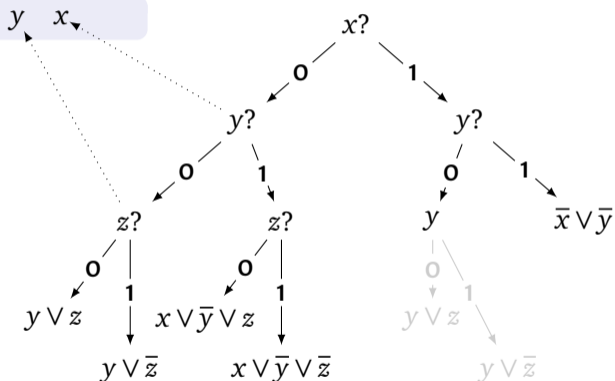
Algorithm 2: CDCL**while not solved do****if conflict then learn()****else if unit then propagate()****else**

maybe forget()

maybe restart()

branch()

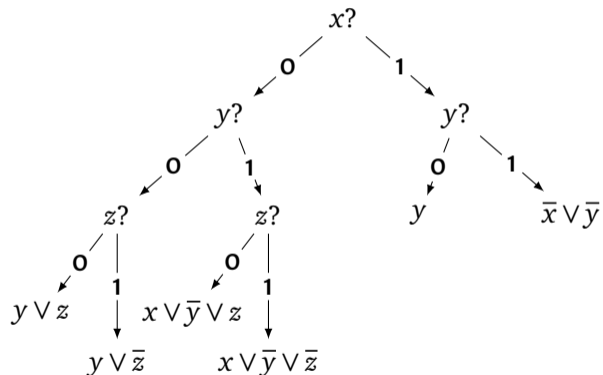
**State: partial assignment
& learned clauses**

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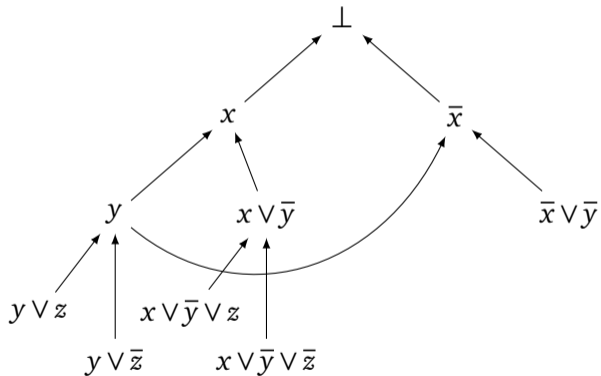
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- ▶ DPLL proofs only in weaker “tree-like” resolution form
 - ▶ There are formulas with polynomial resolution proofs but all tree-like proofs are exponential
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- ▶ Partial results in 2000s
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 - [Van Gelder '05]
 - [Hertel, Bacchus, Pitassi, Van Gelder '08]
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- ▶ Yes (under natural model)
 - [Pipatsrisawat, Darwiche '09]
 - [Atserias, Fichte, Thurley '09]

CDCL equivalent to Resolution: Results

Theorem

[Pipatsrisawat, Darwiche '09]

With **non-deterministic** variable decisions,
CDCL can efficiently find resolution proofs

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With **random** variable decisions,
CDCL can efficiently find **bounded-width** resolution proofs

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CDCL equivalent to Resolution: Simulation

- ▶ Derivation $\pi = C_1, \dots, C_t$.
- ▶ Goal: learn every clause $C_i \in \pi$.

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Example

$$x \vee y \vee z \quad x \vee y \vee \bar{z}$$

$x \vee y$ not absorbed:

- ▶ if $x = 0$ then would propagate y , but DB does not.

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$x \vee y$ is absorbed:

- ▶ if $x = 0$ then propagate $z = 1$ and $y = 1$;
- ▶ if $y = 0$ then propagate $z = 1$ and $x = 1$.

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Algorithm 3: Simulation

for $C_i \in \pi$ **do**

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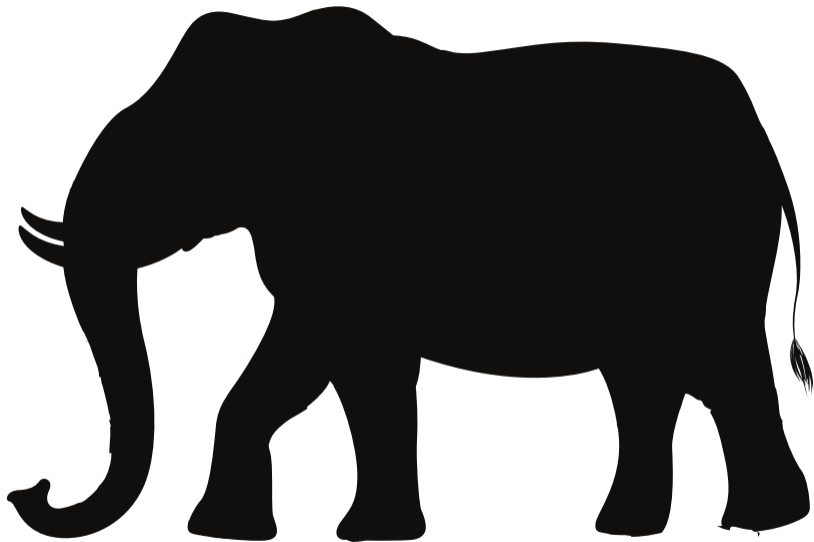
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Branching

Optimal variable choices are needed

- ▶ No deterministic algorithm simulates resolution unless FPT hierarchy collapses.
[Alekhnovich, Razborov '01]
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[Atserias, Müller '19]
- ▶ CDCL with any static order exponentially worse than resolution.
[Mull, Pang, Razborov '19]
- ▶ CDCL with VSIDS and similar heuristics exponentially worse than resolution.
[V'20]

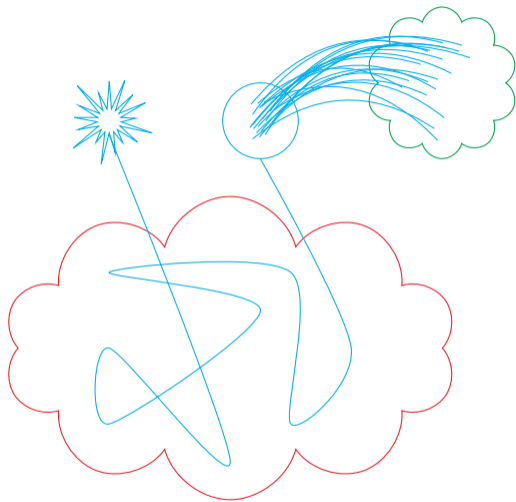
Hard Formulas for VSIDS

- ▶ Give a score $q = q(x)$ to variable x .
- ▶ At each conflict
 - ▶ Bump $q' = q + 1$ if x involved.
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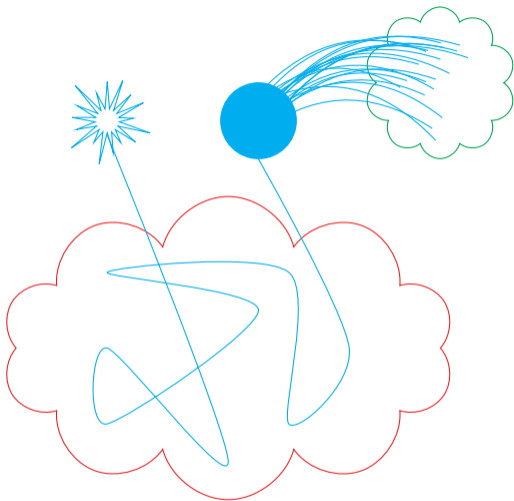
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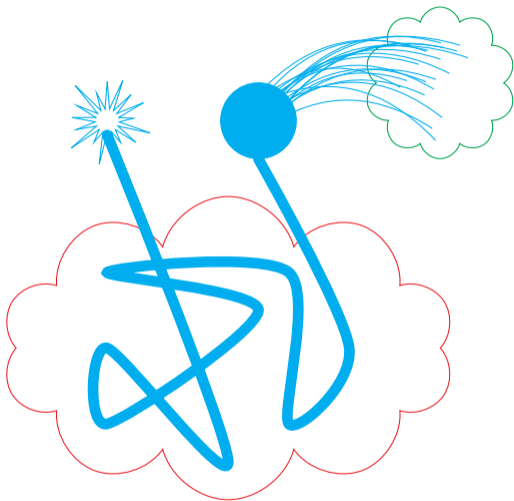
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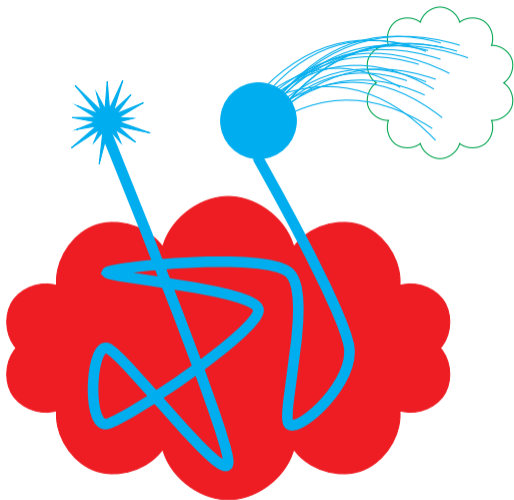
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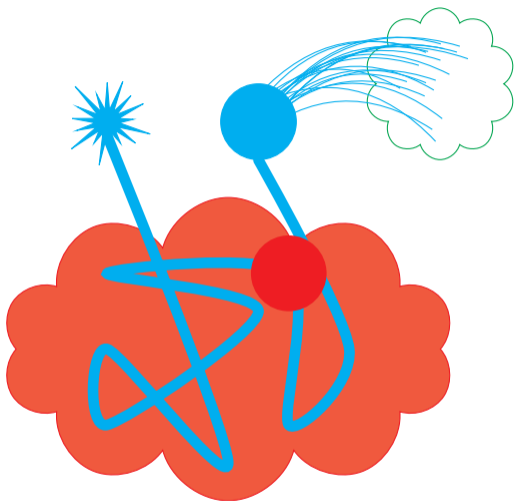
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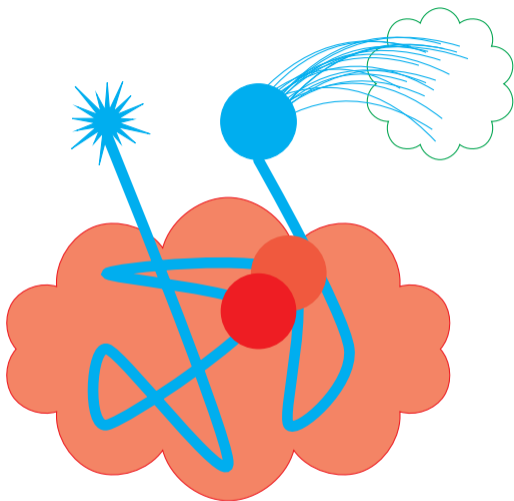
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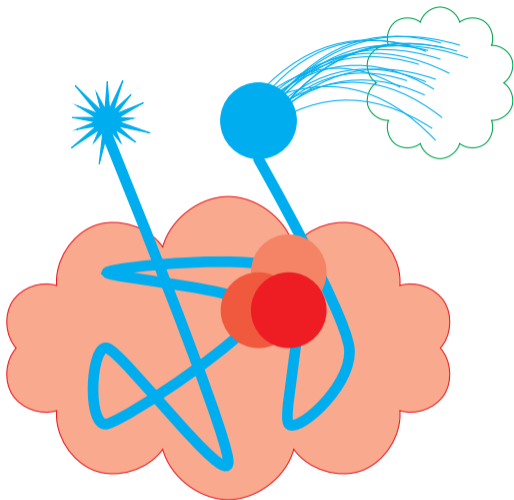
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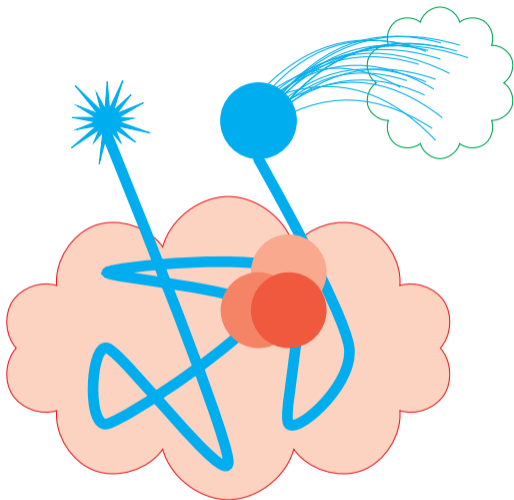
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Throwing Clauses Away

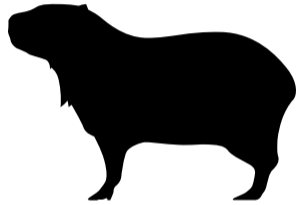
No great theoretical framework

- ▶ With nondeterministic erasures enough to keep only $n \ll L$ clauses in memory.
[Esteban, Torán '01]
- ▶ But more are needed to simulate resolution:
- ▶ Keeping $\ll n$ clauses can exponentially blow-up runtime.
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- ▶ Keeping only narrow clauses can exponentially blow-up runtime.
[Thapen '16]
- ▶ What about clauses with low LBD?



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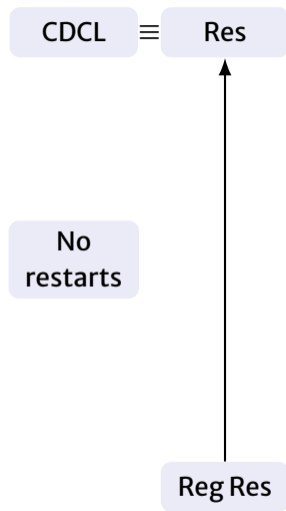
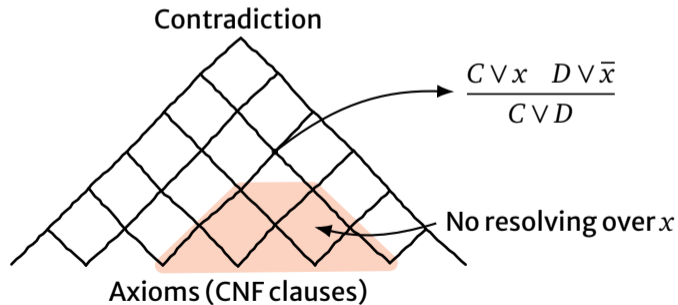
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- ▶ CDCL without restarts between *regular* and *standard* resolution.

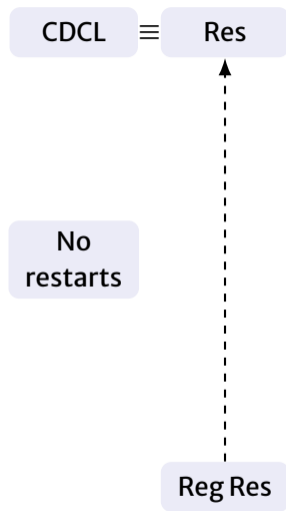
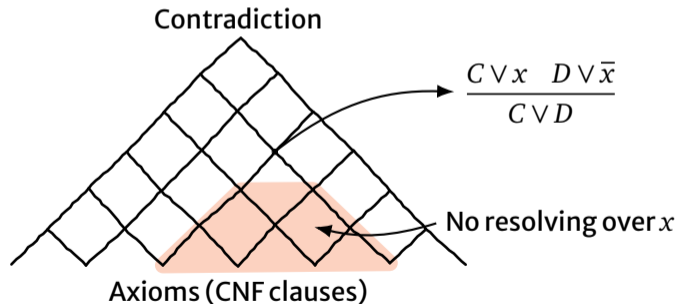
CDCL and Regular Resolution

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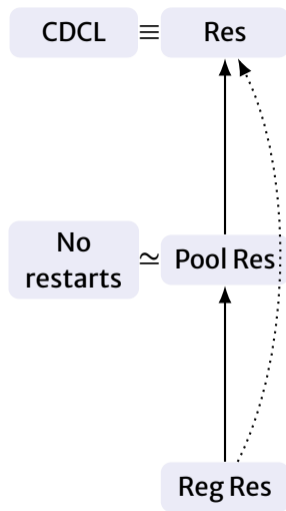
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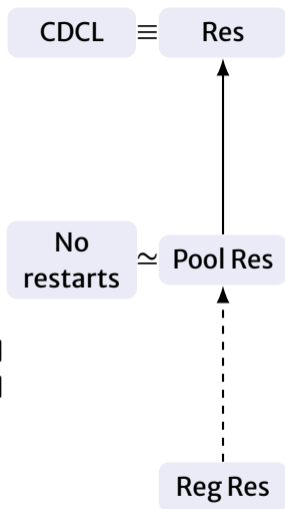


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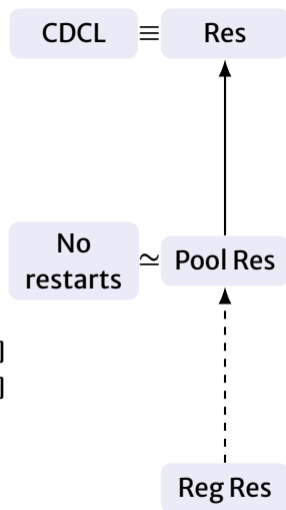
[Bonet, Buss, Johannsen '12]

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[Bonet, Buss, Johannsen '12]
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- ▶ Formula with CDCL proof of length L but requires $L + 1$ w/o restarts?





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- ▶ *C* asserting if unit after backtracking.
- ▶ 1UIP is asserting.

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-
- ▶ Less overhead with decision learning scheme.
 - ▶ Is decision faster than 1UIP?
 - ▶ What overhead is needed?

Merge Resolution

- ▶ A resolution step is a merge if C and D share a literal.

$$\frac{\text{Merge} \quad x \vee y \vee z \quad x \vee y \vee \bar{z}}{x \vee y}$$

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[Andrews '68]

- ▶ Merge resolution 2.0: only reuse merges.

- ▶ 1UIP produces merge resolution proofs.

- ▶ Merge resolution can simulate standard resolution with $O(n)$ overhead.

- ▶ And $\Omega(n)$ overhead sometimes needed.

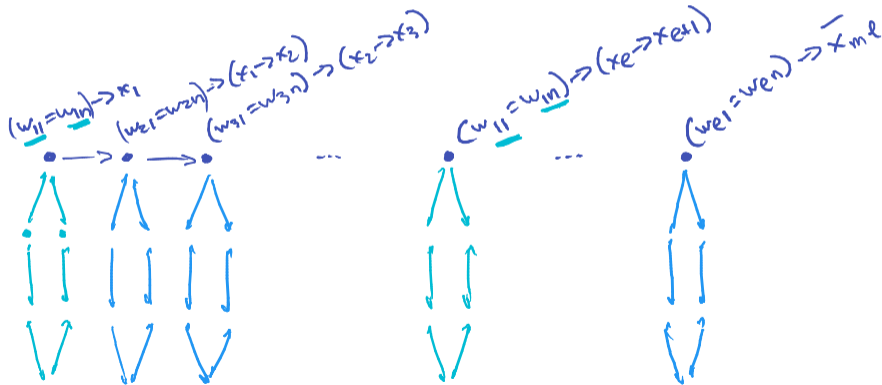
[Fleming, Ganesh, Kolokolova, Li, V]

Tricky Formulas for Merge Resolution

$$\mathcal{W}: \quad w_j^k = w_j^{k+1} \quad \text{for } j \in [\ell], \text{ for } k \in [n-1]$$

$$\mathcal{X}: \quad (w_{\hat{i},1} = w_{\hat{i},n}) \rightarrow (x_{i-1} \rightarrow x_i) \quad \text{for } i \in [m\ell]$$

where $\hat{i} = i \pmod{\ell}$, $x_0 := 1, x_{n\ell} := 0, m \simeq n, \ell \simeq \log n$.



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- ▶ Preprocessing \Rightarrow introduce new variables \Rightarrow extended resolution.
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[Kullmann '99]

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- ▶ Modern solvers use inprocessing, this is now a pressing issue.
- ▶ Can still study DRAT without new variables as a proof system (DRAT^-).
- ▶ Many hard principles for resolution easy in DRAT^- .

[Kullmann '99]

[Buss, Thapen'19]

Take Home

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- ▶ Are restarts important?
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Images: Vecteezy.com

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Thanks!